

Accession number:12620569

Title:Chalcogenide glass surface passivation of a GaAs bipolar transistor for unique avalanche terahertz emitters and picosecond switches

Authors:Vainshtein, S. (1); Javadyan, V. (2); Guoyong Duan (1); Tsendin, K. (3); Hovhannisyan, R. (2); Kostamovaara, J. (1)

Author affiliation:(1) Dept. of Electr. & Inf. Eng., Univ. of Oulu, Oulu, Finland; (2) ENI Inst. of Electron. Mater., Yerevan, Armenia; (3) A.F. Ioffe Physico-Tech. Inst., St. Petersburg, Russia

Source title:Applied Physics Letters

Abbreviated source title:Appl. Phys. Lett. (USA)

Volume:100

Issue:7

Publication date:13 Feb. 2012

Pages:073505 (4 pp.)

Language:English

ISSN:0003-6951

CODEN:APPLAB

Document type:Journal article (JA)

Publisher:American Institute of Physics

Country of publication:USA

Material Identity Number:AB34-2012-012

Abstract:The ultra-narrow “collapsing” field domains discovered recently in avalanching GaAs bipolar junction transistor provide a physical basis for designing unique THz emitters and superfast switches. Reliability in devices operating near their volume breakdown voltage requires decisive suppression of premature surface breakdown. We demonstrate here complete, durable surface breakdown suppression through simple deposition of a massive chalcogenide glass layer on the mesa surface by means of a negative charge formed at the interface.

Number of references:15

Inspec controlled terms:avalanche breakdown - bipolar transistor switches - chalcogenide glasses - gallium arsenide - III-V semiconductors - passivation - semiconductor device breakdown - semiconductor device reliability

Uncontrolled terms:chalcogenide glass surface passivation - unique avalanche THz emitters - picosecond switches - bipolar junction transistor - reliability - volume breakdown voltage - premature surface breakdown - surface breakdown suppression - GaAs

Inspec classification codes:B2560J Bipolar transistors - B2550E Surface treatment (semiconductor technology) - B0170N Reliability

Chemical indexing:GaAs/int As/int Ga/int GaAs/bin As/bin Ga/bin

Treatment:Practical (PRA); Experimental (EXP)

Discipline:Electrical/Electronic engineering (B)

DOI:10.1063/1.3685693

Database:Inspec

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