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Accession number:20120914807650

Title:A 175 GHz HBV frequency quintupler with 60 mW output power

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Source title:IEEE Microwave and Wireless Components Letters

Abbreviated source title:IEEE Microwave Compon. Lett.

Volume:22

Issue:2

Issue date:February 2012

Publication year:2012

Pages:76-78

Article number:6140932

Language:English

ISSN:15311309

CODEN:IMWCBJ

Document type:Journal article (JA)

Publisher:Institute of Electrical and Electronics Engineers Inc., 445 Hoes Lane / P.O. Box 1331, Piscataway, NJ 08855-1331, United States

Abstract:In this letter, we present a fixed tuned 175 GHz frequency quintupler with 60 mW output power. The peak efficiency is 6.3% and the 3 dB bandwidth is 8 GHz. The multiplier is based on a single Heterostructure Barrier Varactor (HBV) diode that is flip-chip soldered into a microstrip matching circuit. All the matching is done "on-chip" and there is no need for dc bias. The multiplier block is very compact (25 &times; 9 &times; 8 mm<sup>3</sup>). &copy; 2006 IEEE.

Number of references:13

Main heading:DC power transmission

Controlled terms:Condensed matter physics - Harmonic generation - Microwave integrated circuits - Millimeter waves - Varactors

Uncontrolled terms:3 dB bandwidth - DC bias - Flip chip - Frequency multiplier - GHz frequencies - heterostructure barrier varactor (HBV) - Heterostructure barrier varactors - Matching circuit - Multiplier blocks - MW output - On chips - Peak efficiency - Quintupler - Terahertz sources

Classification code:706.1.1 Electric Power Transmission - 711 Electromagnetic Waves - 714.2 Semiconductor Devices and Integrated Circuits - 751.1 Acoustic Waves - 931 Classical Physics; Quantum Theory; Relativity - 933 Solid State Physics

DOI:10.1109/LMWC.2011.2181494

Database:Compendex

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