234

Accession number:20122915263189

Title:Assessing charge carrier trapping in silicon nanowires using picosecond conductivity measurements

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Source title:Nano Letters

Abbreviated source title:Nano Lett.

Volume:12

Issue:7

Issue date:July 11, 2012

Publication year:2012

Pages:3821-3827

Language:English

ISSN:15306984

E-ISSN:15306992

CODEN:NALEFD

Document type:Journal article (JA)

Publisher: American Chemical Society, 2540 Olentangy River Road, P.O. Box 3337, Columbus, OH 43210-3337, United States

Abstract:Free-standing semiconductor nanowires on bulk substrates are increasingly being explored as building blocks for novel optoelectronic devices such as tandem solar cells. Although carrier transport properties, such as mobility and trap densities, are essential for such applications, it has remained challenging to quantify these properties. Here, we report on a method that permits the direct, contact-free quantification of nanowire carrier diffusivity and trap densities in thin (∼25 nm wide) silicon nanowires - without any additional processing steps such as transfer of wires onto a substrate. The approach relies on the very different terahertz (THz) conductivity response of photoinjected carriers within the silicon nanowires from those in the silicon substrate. This allows quantifying both the picosecond dynamics and the efficiency of charge carrier transport from the silicon nanowires into the silicon substrate. Varying the excitation density allows for quantification of nanowire trap densities: for sufficiently low excitation fluences the diffusion process stalls because the majority of charge carriers become trapped at nanowire surface defects. Using a model that includes these effects, we determine both the diffusion constant and the nanowire trap density. The trap density is found to be orders of magnitude larger than the charge carrier density that would be generated by AM1.5 sunlight. & copy; 2012 American Chemical Society.

Number of references:27

Main heading:Nanowires

Controlled terms: Charge carriers - Defects - Optoelectronic devices - Solar cells - Substrates -

Terahertz spectroscopy - Transport properties

Uncontrolled terms:Building blockes - Bulk substrates - Carrier diffusivity - Charge carrier trapping - Conductivity measurements - Diffusion constant - Diffusion process - Excitation density - Fluences - Nanowire surface - Orders of magnitude - Photoinjected carriers - Picosecond dynamics - Picoseconds - Processing steps - Semiconductor nanowire - Silicon nanowires - Silicon substrates - Surface passivation - Tandem solar cells - Terahertz - Trap density

Classification code:951 Materials Science - 933 Solid State Physics - 931.2 Physical Properties of Gases, Liquids and Solids - 931.1 Mechanics - 801 Chemistry - 761 Nanotechnology - 717.2 Optical Communication Equipment - 701.1 Electricity: Basic Concepts and Phenomena - 615.2 Solar Power - 461 Bioengineering and Biology - 423 Non Mechanical Properties and Tests of Building Materials

DOI:10.1021/nl3017835

Database:Compendex

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